



ECE 111

Electronic Engineering Fundamentals

2015-2016

Semiconductors

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- semiconductors and their structure
- production of silicon
- band diagram
- intrinsic carrier concentration
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Semiconductor Devices



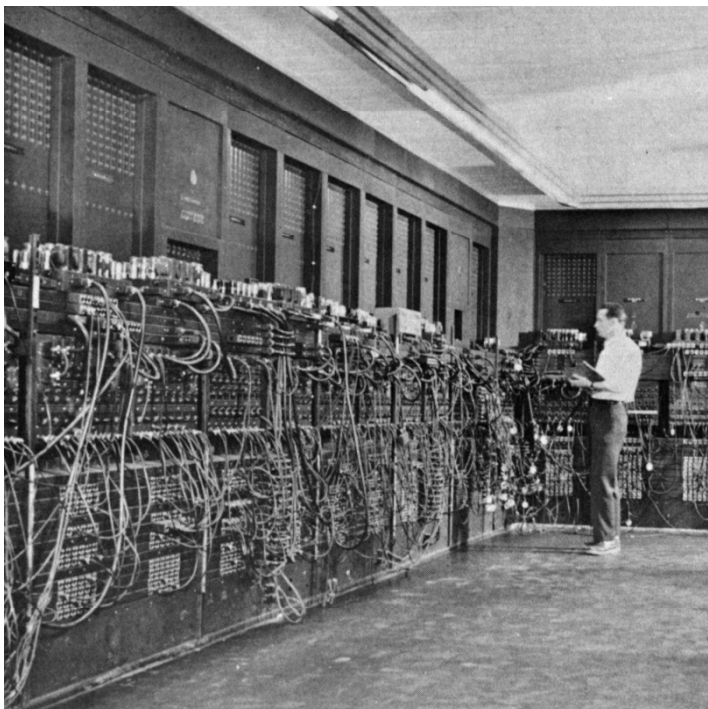
Semiconductor devices are [electronic components](#) that exploit the [electronic](#) properties of [semiconductor](#) materials, principally [silicon](#), [germanium](#), and [gallium arsenide](#).

Semiconductor devices have replaced [thermionic devices](#) (vacuum tubes) in most applications. They use [electronic conduction](#) in the [solid state](#) as opposed to the [vacuum state](#) or [gaseous state](#).

Semiconductor devices are available as discrete units (such as those sold in electronics stores) or can be integrated along with a large number — often millions — of similar devices onto a single chip, called an [integrated circuit](#) (IC).

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The first computers using vacuum tubes



The first computers using **vacuum tubes** appeared around 1945 : (ENIAC.)

They were quite power hungry and heavy machines. The ENIAC contained around 17500 vacuum tubes as well as 1500 relays.

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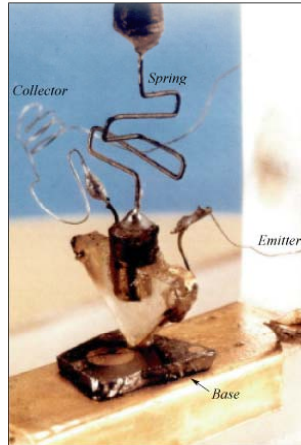
First transistor



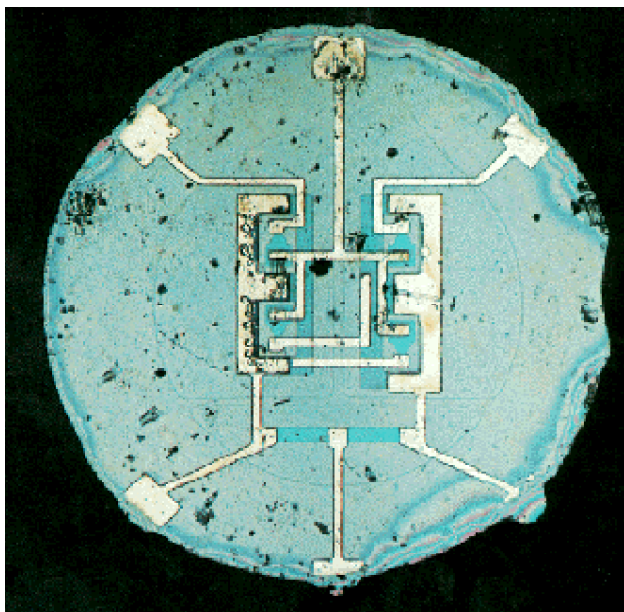
1947

Picture shows a point-contact transistor structure comprising the plate of n-type germanium and two line-contacts of gold supported on a plastic wedge.

The first point contact transistor
William Shockley, John Bardeen, and Walter Brattain
Bell Laboratories, Murray Hill, New Jersey (1947)



First monolithic integrated circuit

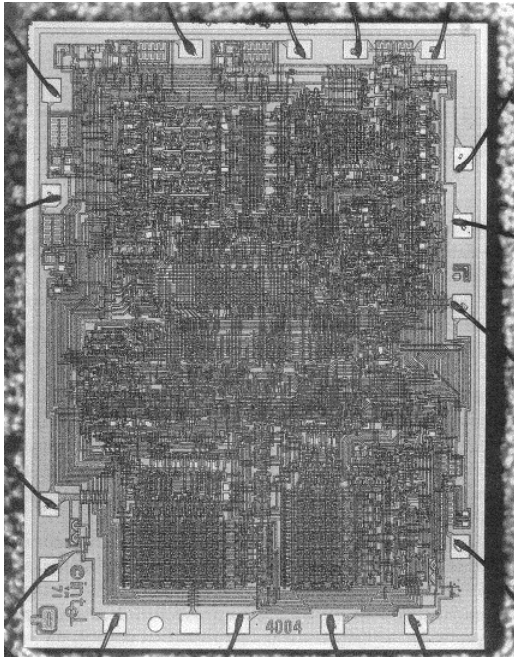


1961

Picture shows a flip-flop circuit containing 6 devices, produced in planar technology.

Source:
R. N. Noyce,
"Semiconductor device-and-lead structure",
U.S. Patent 2,981,877

First microprocessor



1971

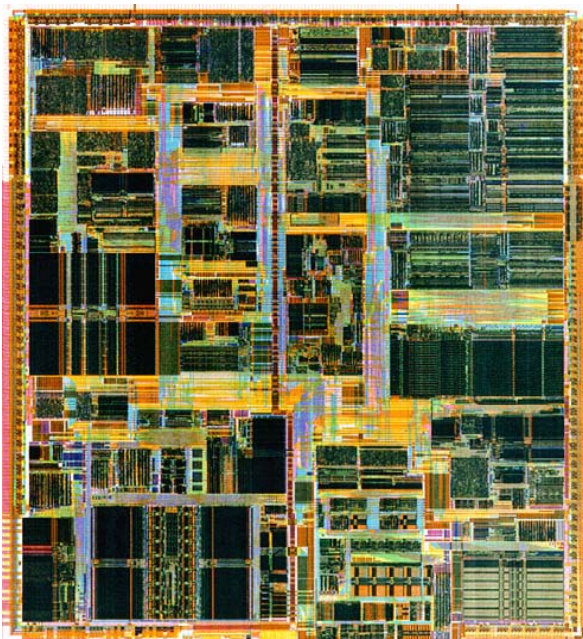
Picture shows a four-bit microprocessor *Intel 4004*.

- 10 μm technology
- 3 mm \times 4 mm
- 2300 MOS-FETs
- 108 kHz clock frequency

Source:

Intel Corporation

Pentium IV processor



2001

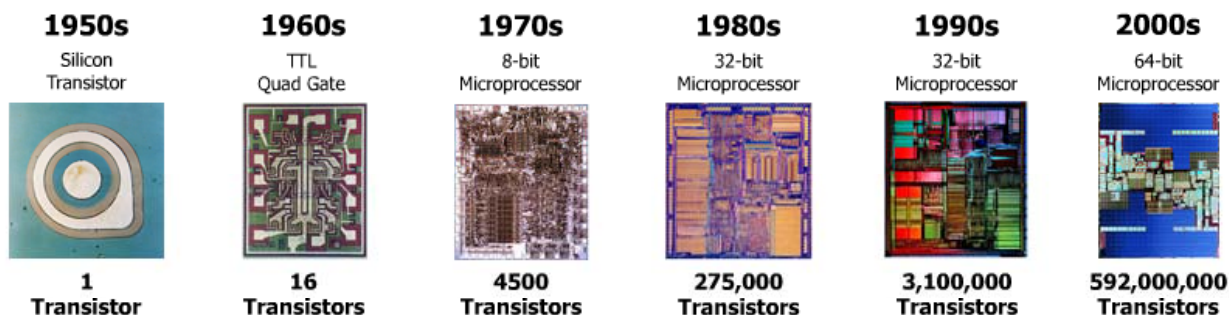
Picture shows a ULSI chip with 32-bit processor *Intel Pentium 4*.

- 0.18 μm CMOS technology
- 17.5 mm \times 19 mm
- 42 000 000 components
- 1.6 GHz clock frequency

Source:

Intel Corporation

MOORE'S LAW "Transistor density on integrated circuits doubles about every two years." *



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Semiconductors

Period	II	III	IV	V	VI
2		B	C	N	O
3	Mg	Al	Si	P	S
4	Zn	Ga	Ge	As	Se
5	Cd	In	Sn	Sb	Te
6	Hg		Pb	Bi	



III-V semiconductors

Period	II	III	IV	V	VI
2		B	C	N	O
3	Mg	Al	Si	P	S
4	Zn	Ga	Ge	As	Se
5	Cd	In	Sn	Sb	Te
6	Hg		Pb	Bi	

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II-VI semiconductors

Period	II	III	IV	V	VI
2		B	C	N	O
3	Mg	Al	Si	P	S
4	Zn	Ga	Ge	As	Se
5	Cd	In	Sn	Sb	Te
6	Hg		Pb	Bi	

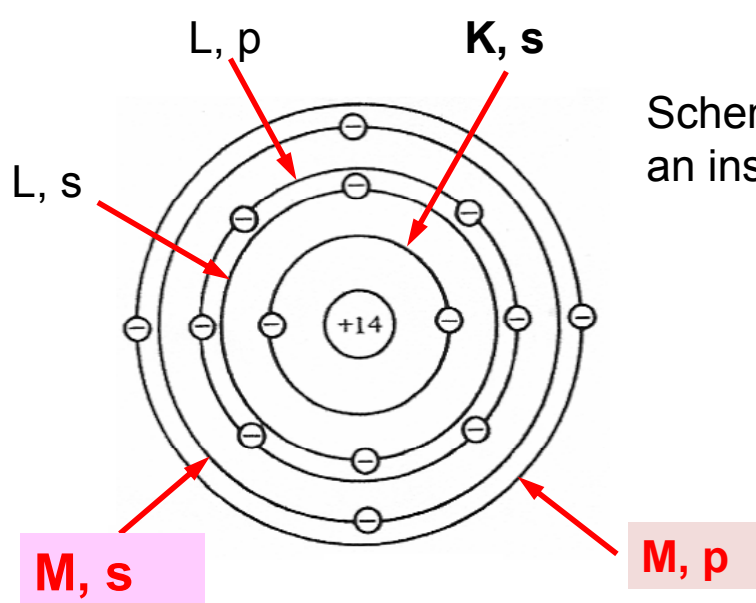
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Electron shells and sub-shells

shell n	K 1	L 2		M 3			N 4			
sub-shell l	s 0	s 0	p 1	s 0	p 1	d 2	s 0	p 1	d 2	f 3
electron number	2	2	6	2	6	10	2	6	10	14
	2	8		18			32			

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atomic structure of silicon

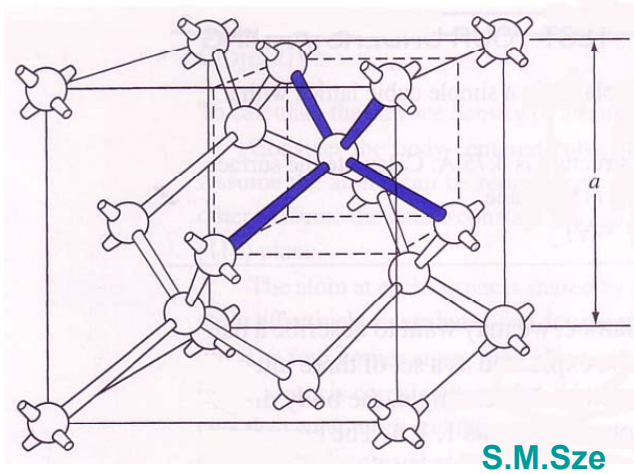


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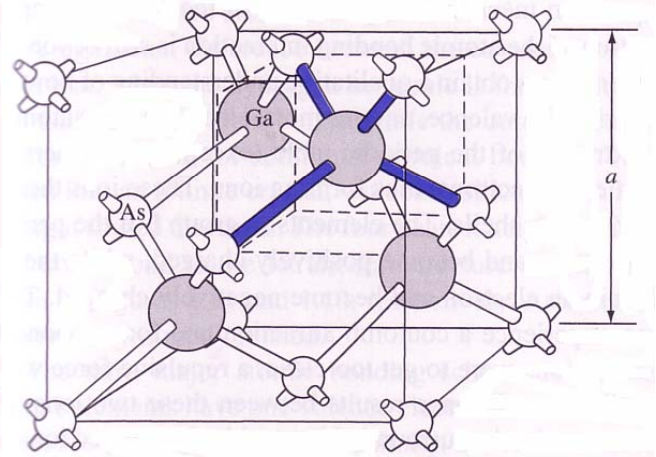
Crystal structure



Si: diamond lattice

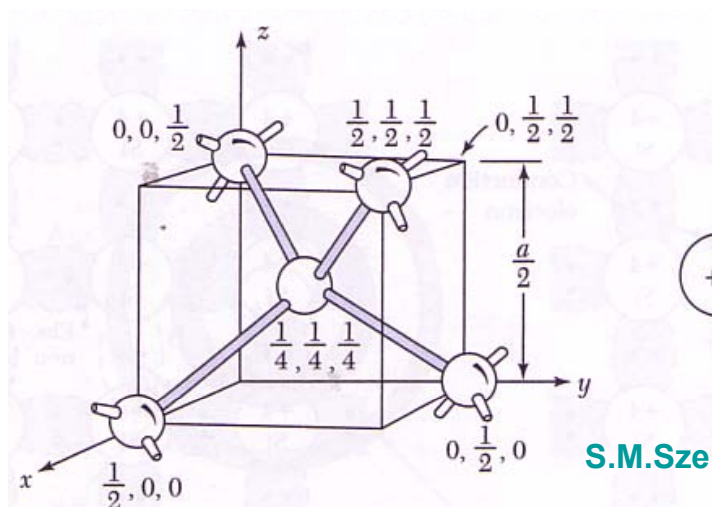


GaAs: zincblende lattice

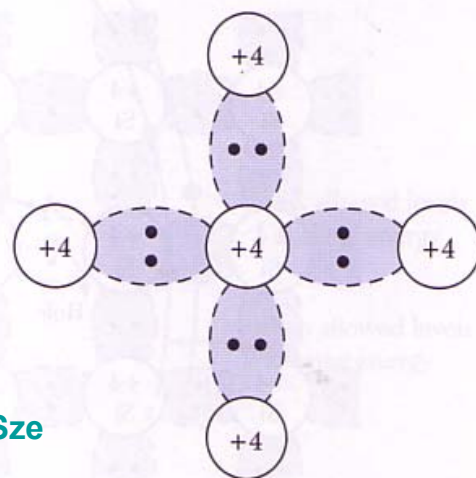


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Tetrahedron bond and lattice



3D structure
covalent bonding



2D representation
electron pair

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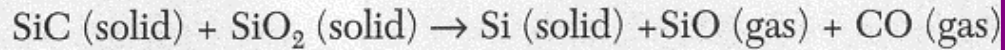
Silicon production



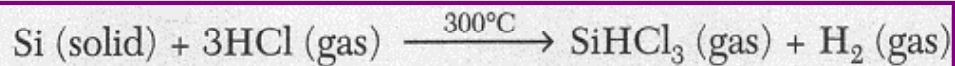
production steps of silicon

“95% of materials used by the electronic industry is silicon”

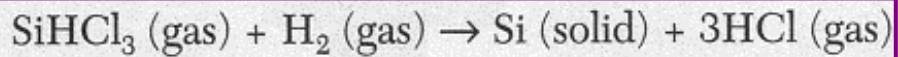
SiO₂ sand (quartzite)



98% pure silicon



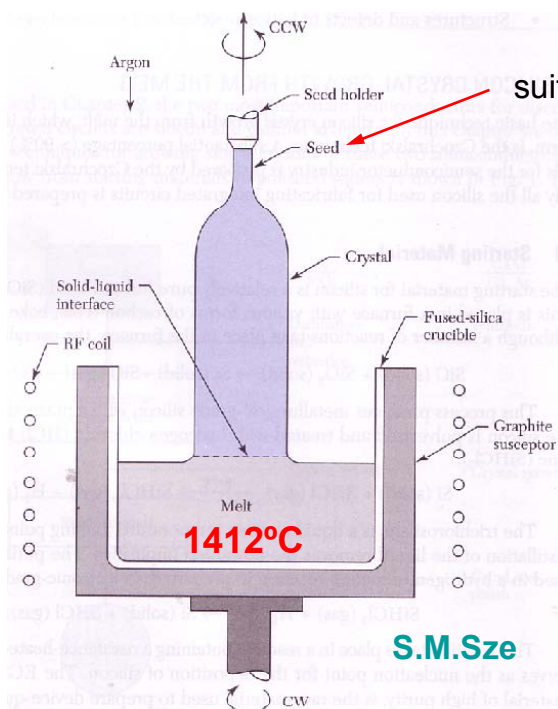
gaseous trichlorosilane



rods of ultrapure polycrystalline silicon

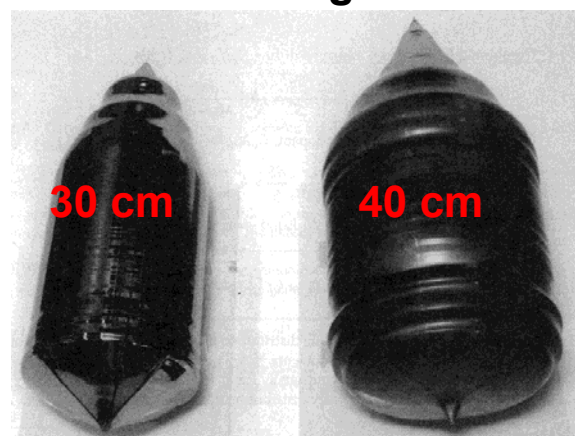
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Czochralski technique

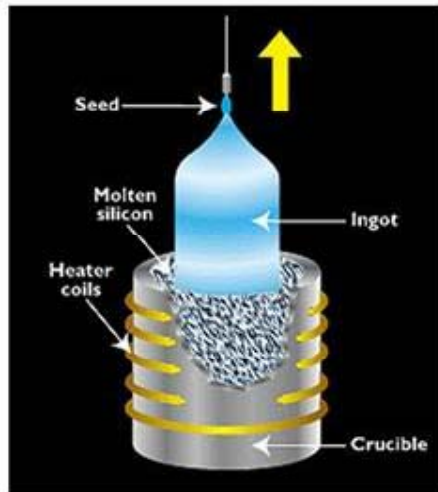
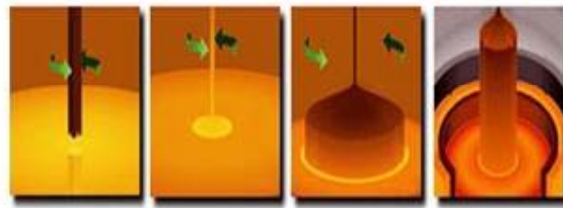


suitable orientation (e.g. [111])

silicon ingots



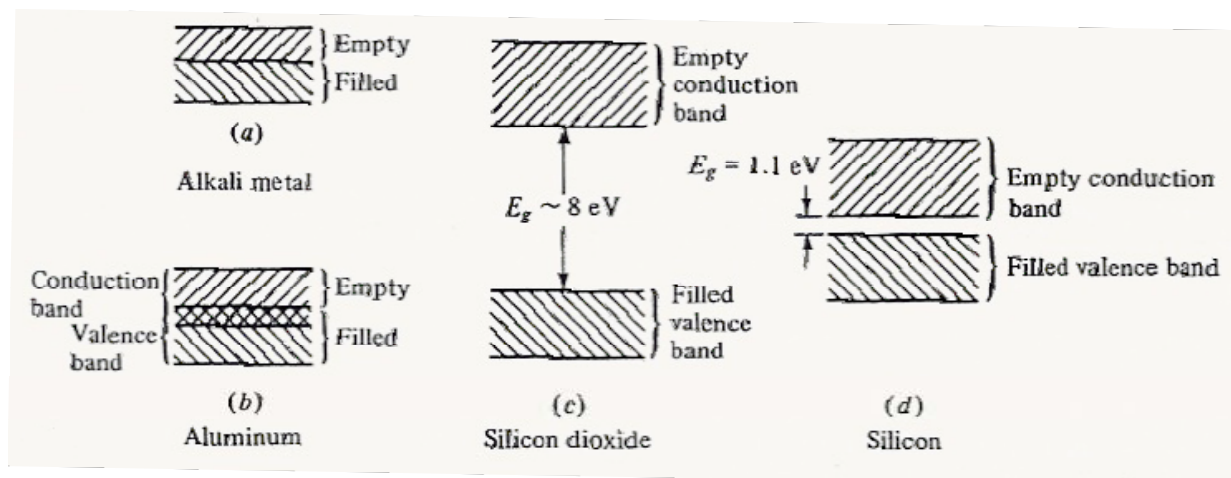
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Band diagram

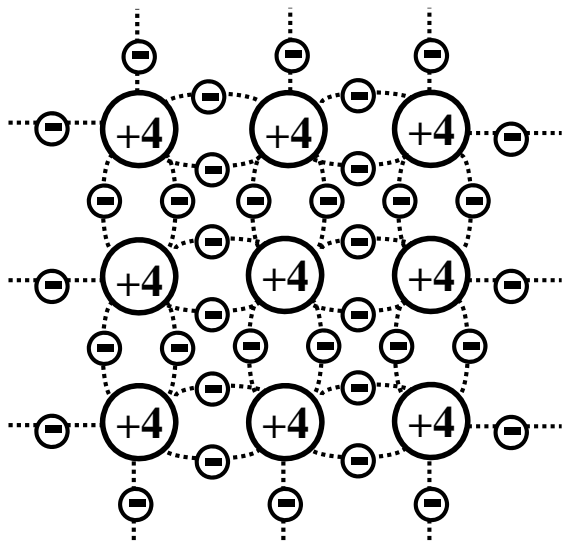
energy band representation



S.M.Sze

Schematic energy band representations for different materials

Intrinsic semiconductors



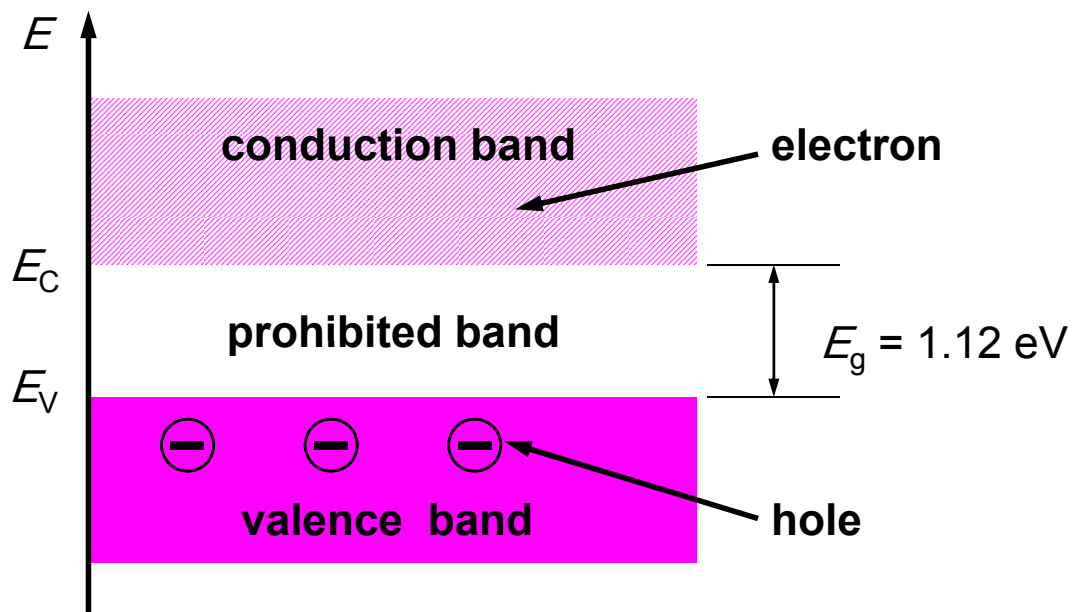
valence bonds model

properties:

- No impurities – only tetrahedron bonds.
- All bonds complete at 0K.
- Part of electrons from tetrahedron bonds is released at higher temperatures.

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electrons and holes in Si



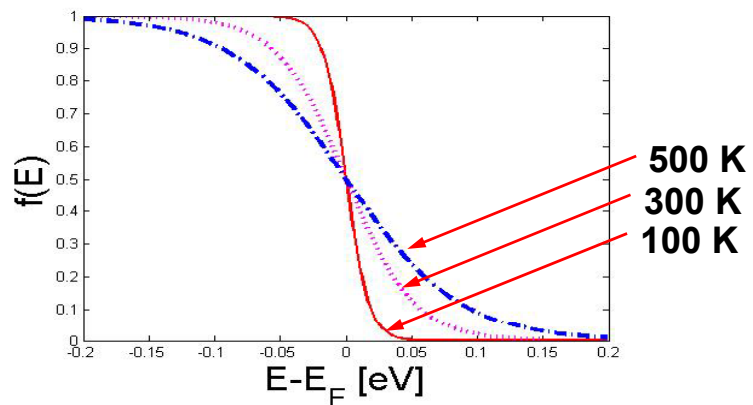
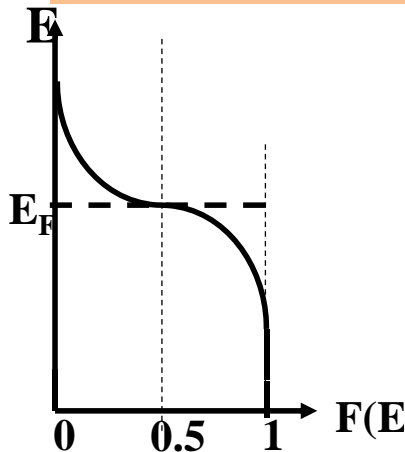
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Fermi distribution function



$$F(E) = \frac{1}{1 + e^{(E - E_F)/kT}}$$

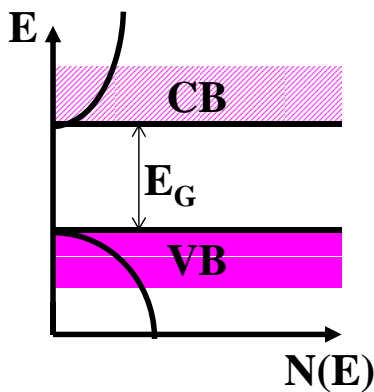
E_F Fermi level
 K Boltzmann's constant **$1.38 \times 10^{-23} \text{ J/K}$**
 T temperature in kelvins



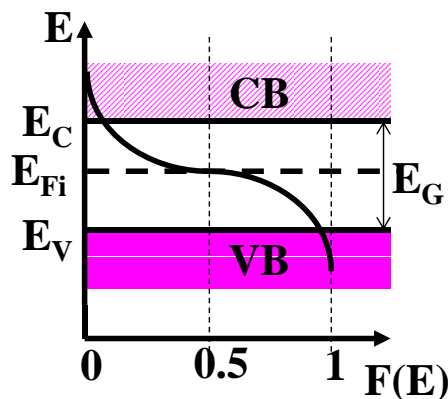
carrier concentration



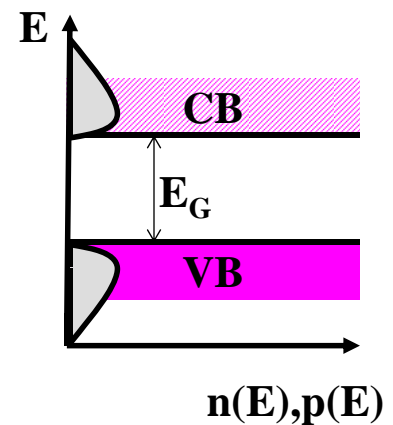
density of states



Fermi distribution function



carrier concentration



$$n = p = n_i = \int_{E_C}^{\infty} f(E) N(E) dE$$

Density of energy states



Density of states in the conduction band: $N(E) = \frac{4\pi}{h^3} (2m_e)^{3/2} \sqrt{E - E_c}$

Density of states in the valence band: $N(E) = \frac{4\pi}{h^3} (2m_h)^{3/2} \sqrt{E_v - E}$

where: h – Planck's constant **$6.62 \times 10^{-34} \text{ J.s}$**
 m_e – effective mass of electron
 m_h – effective mass of hole

Total number of electrons in the conduction band:

$$n = \int_{E_c}^{\infty} f(E) N(E) dE = N_C e^{E_F - E_C / kT}$$

N_C – effective density of states in the conduction band

$$N_C = \frac{4\sqrt{2}}{h^3} (KT \pi m_e^*)^{3/2}$$

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Holes in valence band:

The total number of holes in the valence band:

$$p = \int_{-\infty}^{E_v} [1 - f(E) N(E)] dE = N_V e^{E_v - E_F / kT}$$

N_V – effective density of states in the valence band

$$N_V = \frac{4\sqrt{2}}{h^3} (KT \pi m_h^*)^{3/2}$$

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Fermi level in intrinsic semiconductor



assuming: $n = p = n_i$

from: $n = N_C e^{-(E_C - E_F)/kT}$

and: $p = N_V e^{-(E_F - E_V)/kT}$

$$E_{Fi} = \frac{E_C + E_V}{2} + \frac{kT}{2} \ln \frac{N_V}{N_C}$$

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Intrinsic carrier density



$$n = p = n_i$$

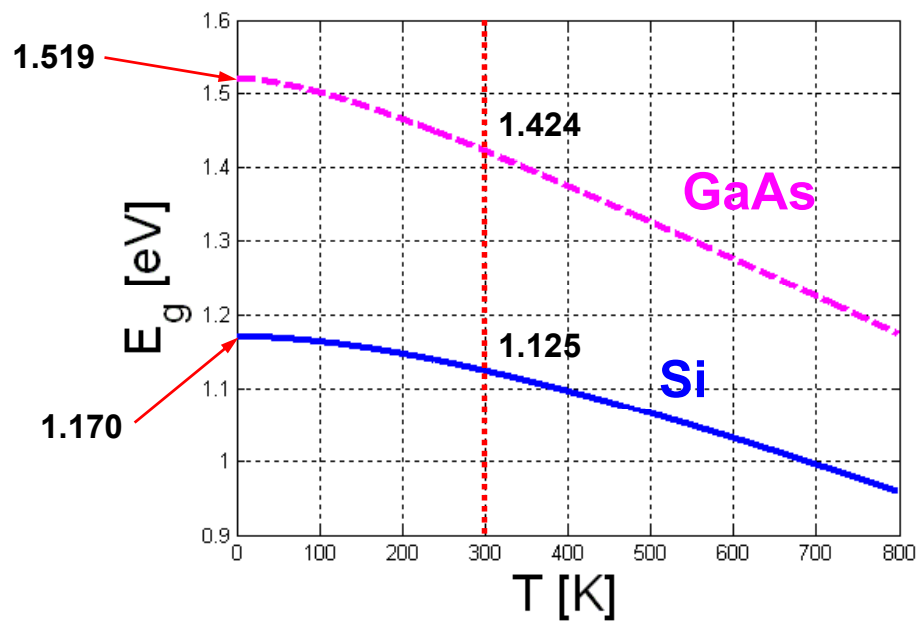
$$np = n_i^2$$

$$pn = N_C N_V e^{-E_g/kT}$$

$$n_i = \sqrt{N_C N_V} \exp\left(-E_g/2kT\right)$$

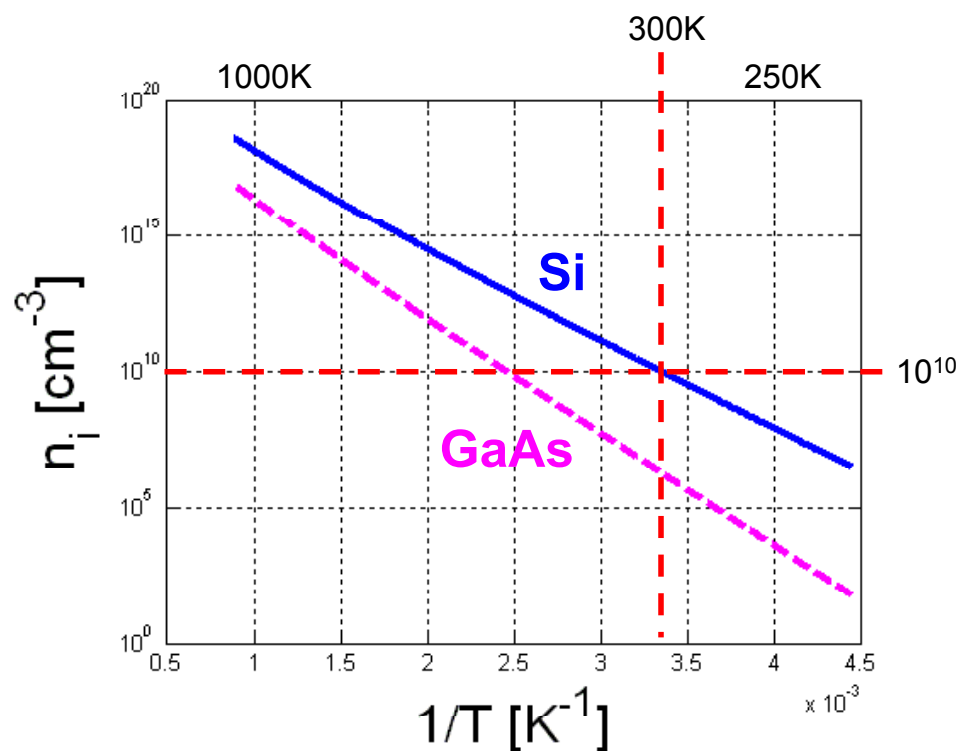
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Influence of temperature on band gap



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Influence of temperature



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